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EJ Pencoske
Attorney for Applicant



Date: 12 July 2007

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. : 7,139,203
Application No. : 10/678,595
Applicant : HUBER, Brian W.
Filed : 03 October 2003
Titled : Voltage Regulator and Data Path for a Memory Device
Art Unit : 2824
Examiner : HUR, Jung H.
Atty. Docket No. : PAT000859-007

TRANSMITTAL LETTER

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Respectfully submitted,

EJ Pencoske

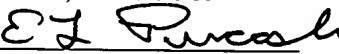
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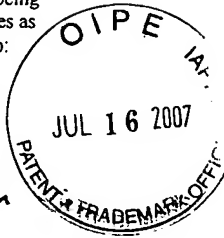
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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**Page 1 of 1

PATENT NO. : 7,139,203 B2
APPLICATION NO.: 10/678,595
ISSUE DATE : 21 November 2006
INVENTOR(S) : HUBER, Brian W.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 3, Line 65, delete "pre-river" and substitute therefore --pre-driver--.

Column 11, Line 12, delete "180" and substitute therefore --180°--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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